

REMARKS

Reconsideration of this application, as amended, is respectfully requested.

Claims 1, 3, 5, and 48-60 are pending. Claims 1, 3, 5, and 48-60 stand rejected.

Claims 1, 48, 54, and 56 have been amended. No claims have been canceled. No claims have been added. Support for the amendments is found in the specification, the drawings, and in the claims as originally filed. Applicants submit that the amendments do not add new matter.

REJECTIONS UNDER 35 U.S.C. § 103

Claims 1-3, 5 and 48-60 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over U.S. Patent No. 5,714,870 to Dunstan ("Dunstan").

Amended claim 1 reads as follows:

A method comprising:

reading a time of exiting a reduced power consumption state prior to exiting the reduced power consumption state in response to an interrupt;

storing the time of exiting of the reduced power consumption state in a register; prior to exiting the reduced power consumption state;

after the reading and the storing the time of exiting, allowing an interrupt routine associated with the interrupt to execute to exit the reduced power consumption state;

and

calculating a reduced power consumption state duration based on the time of exiting the reduced power consumption state stored in the register.

(Amended claim 1)(emphasis added)

Dunstan, in fact, discloses the following:

To measure the power consumed by host 1 during a suspended period, power consumption monitor 4 is configured to detect when host 1 is about to suspend. The accuracy of this determination is improved by invoking power consumption monitor 4 as closely as possible to the time host 1 enters a suspended condition, and again as closely as possible to the time host 1 exits the suspended condition.

Any method of identifying initiation of a suspend may be used, and typically involves detecting an interrupt causing a processor to enter a reduced-power mode of operation.

(Dunstan, col. 5, lines 29-39)(emphasis added)

In particular, Dunstan discloses

An electronic device configured for monitoring power consumed while said electronic device is in a reduced-power condition, said electronic device comprising an electrical energy storage unit powering said electronic device, a processor, a memory, a display device, and a power consumption monitor, said power consumption monitor including a means for respectively determining when said electronic device enters and exits the reduced-power state, a means for determining a charge capacity of said electrical energy storage unit before and after said electronic device is in the reduced-power state, a means for determining a period of time said electronic device is in the reduced-power state, and a means for calculating charge capacity lost by said electrical energy storage unit during the period of time that the electronic device is in the reduced-power state.

(Dunstan, col. 9, lines 27-41) (emphasis added)

Thus, Dunstan merely discloses means for determining when the electronic device enters and exits the reduced-power state. In contrast, amended claim 1 refers to exiting the reduced

power consumption state after reading and storing the time of exiting the reduced power state.

Dunstan, in fact, fails to disclose, teach, or suggest reading a time of exiting a reduced power

consumption state prior to exiting the reduced power consumption state in response to an

interrupt; storing the time of exiting of the reduced power consumption state in a register prior to

exiting the reduced power consumption state; and after reading and storing the time of exiting,

allowing an interrupt routine associated with the interrupt to execute to exit the reduced power

consumption state, as recited in amended claim 1.

Therefore, applicant respectfully submits that amended claim 1 is not obvious under 35 U.S.C. § 103(a) over Dunstan.

Given that amended independent claims 48, 54, and 56 contain at least the discussed limitations, applicant respectfully submits that claims 48, 54, and 56 are not obvious under 35 U.S.C. § 103(a) over Dunstan.

Given that claims 2-3, 49-53, 55, and 57-60 depend from amended independent claims 48, 54, and 56 and add additional limitations, Applicant respectfully submits that claims 2-3, 49-53, 55, and 57-60 are likewise not obvious under 35 U.S.C. § 103(a) over Dunstan.

CONCLUSION

It is respectfully submitted that in view of the amendments and arguments set forth herein, the applicable rejections and objections have been overcome. If the Examiner believes a telephone conference would expedite or assist in the allowance of the present application, the Examiner is invited to call the undersigned at (408) 720-8300.

If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

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Date: May 29, 2007

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Judy L. Steinkraus
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05/29/2007

Application No.: 10/037,184
Date Mailed: 05/29/2007
Client: Intel Corporation
Title: AUTOMATIC READ OF CURRENT TIME WHEN EXITING LOW-POWER STATE
First Named Inventor: David I. Poisner
The following has been received in the U.S.P.T.O. on the date stamped hereon:

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☐ Examiner's Interview Summary
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Filing Date: 01/02/2002
Due Date: 05/28/2007
Docket No.: 42P12974
Atty/Sec: MJM AKA TVR jxs

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